



Coras. and Ma  
**BOX AF**

AF/3728  
#7/B(2)  
AMENDMENT UNDER 37 C.F.R. §1.116  
EXPEDITED PROCEDURE  
GROUP ART UNIT 3728  
PATENT APPLICATION  
Q59017  
9/24/01

**IN THE UNITED STATES PATENT & TRADEMARK OFFICE**

In re application of

Shigeru SEMBONMATSU, et al.

Appln. No.: 09/559,348

Confirmation No.: to be determined

Group Art Unit: 3728

Examiner: Shian T. Luong

Filed: April 27, 2000

For: TRAY FOR SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**AMENDMENT UNDER 37 C.F.R. §1.116**

**BOX AF**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated May 18, 2001, please amend the above-identified application as follows:

**IN THE CLAIMS:**

**Please enter the following amended claims:**

1. (Twice Amended) A tray for storing a semiconductor integrated circuit device having a package and wiring terminals on a lower surface of the package, said tray comprising:

TECHNOLOGY CENTER R3700

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